



# **HIGH VOLTAGE THIN FILM CHIP RESISTORS**

VT series 0.1% TO 1%, TC10 TO TC50 sizes 1206 **RoHS** compliant



1101

RUL

2R20

62

1221

221



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## <u>SCOPE</u>

This specification describes VT1206 high precision-high stability chip resistors made by thin film process.

## APPLICATIONS

- Automotive electronics
- Industrial and medical equipment
- Test and measuring equipment
- Telecommunications

#### FEATURES

- Maximum operating voltage up to 700V
- AEC-Q200 qualified
- Total lead free without RoHS exemption
- Halogen free epoxy
- Superior resistance against sulfur containing atmosphere
- Moisture sensitivity level: MSL I
- Reducing environmentally hazardous wastes
- High component and equipment reliability
- Non-forbidden materials used in products/production

#### ORDERING INFORMATION - GLOBAL PART NUMBER

Part number is identified by the series name, size, tolerance, packaging type, temperature coefficient, taping reel and resistance value.

## **GLOBAL PART NUMBER**

VΤ	<u>XXXX</u>	<u>X</u>	<u>X</u>	<u>X</u>	<u>XX</u>	<u>XXXXX</u>	L	
	(1)	(2)	(3)	(4)	(5)	(6)	(7)	

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#### (2) TOLERANCE

 $B = \pm 0.1\%$   $C = \pm 0.25\%$  $D = \pm 0.5\%$ 

 $F = \pm 1\%$ 

## (3) PACKAGING TYPE

R = Paper taping reel

#### (4) TEMPERATURE COEFFICIENT OF RESISTANCE

В	= ±	10	ppm/°
D	= ±	25	ppm/°C

 $E = \pm 50 \text{ ppm/°C}$ 

#### (5) TAPING REEL

07 = 7 inch dia. Reel

## (6) RESISTANCE VALUE

There are 2~4 digits indicated the resistor value.

Letter K/M is decimal point

Example: 499K=499,000Ω

IM=1,000,000Ω

#### (7) DEFAULT CODE

Letter L is the system default code for ordering only. (NOTE)

#### **ORDERING EXAMPLE**

The ordering code of a VT1206 chip resistor, TCR 25 value 560K $\Omega$  with  $\pm0.5\%$  tolerance, supplied in 7-inch tape reel is: VT1206DRD07560KL.

#### NOTE

- I. All our Rchip products meet RoHS compliant and Halogen Free. "LFP" of the internal 2D reel label mentions "Lead Free Process".
- 2. On customized label, "LFP" or specific symbol can be printed.



#### MARKING

#### VT1206



Both E-24 and E-96 series: 4 digits First three digits for significant figure and 4th digit for number of zeros

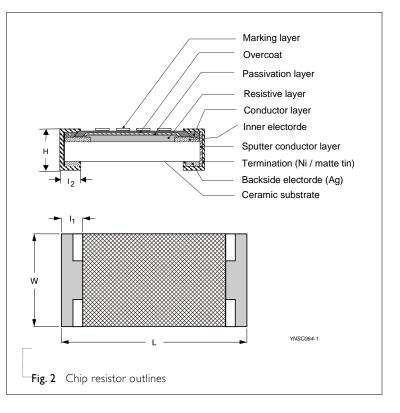
#### NOTE

For further marking information, please see special data sheet "Chip resistors marking".

#### **CONSTRUCTION**

The resistors are constructed out of a high grade ceramic body. Internal metal electrodes are added at each end connected by a resistive layer. This resistive layer is trimmed to its nominal value and on both ends a contact is made which will guarantee optimum solderability. This is achieved by applying several layers and for ease of soldering the outer layer consists of Ni/matte tin. Adding a special protective layer, passivation coating, on this series to enhance moisture resistance of the environment.

#### OUTLINES



Chip Resistor Surface Mount	VT	SERIES	1206
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# **DIMENSIONS**

Table I

TYPE	L (mm)	W (mm)	H (mm)	l⊤ (mm)	l₂ (mm)
VT1206	3.10 ±0.10	1.60 ±0.10	0.55 ±0.10	0.45 ±0.20	0.40 ±0.20

# ELECTRICAL CHARACTERISTICS

Table 2								
			Max.	Resistance	Range (E-24/E-	- <b>96</b> series)( Ω	) & Tolerance	و(۱)
	Operating	Power	Working	T.C.R.	±0.1%	±0.25%	±0.5%	±I%
TYPE	Temperature Range	Rating	Voltage	(ppm/°C) <sup>(2)</sup>	(B)	(C)	(D)	(F)
				±50 (E)				
VT1206	–55 °C to +155 °C	1/4W	700 V	±25 (D)		$I62K \le R$	≤IM5	
				±10 (B)				

**NOTE** : I. Global part number (code 7)

2. Global part number (code 9)

3. Rated voltage follow maximum voltage formula.

V=  $\sqrt{(P \times R)}$ 

V: Rated Voltage (V), P: Rated Power(W), R: Resistance Value( $\Omega$ )

## FOOTPRINT AND SOLDERING PROFILES

For recommended footprint and soldering profiles, please see the special data sheet "Chip resistors mounting".

## PACKING STYLE AND PACKAGING QUANTITY

**Table 3** Packing style and packaging quantity

PRODUCT TYPE	PATKING STYLE	<b>REEL DIMENSION</b>	QUANTITY PER REEL
VT1206	Paper taping reel	7" (178 mm)	5,000 Units

NOTE: for paper tape and reel specification/dimensions, please see the special data sheet "packing" document.

#### FUNCTIONAL DESCRIPTION

#### **OPERATING TEMPERATURE RANGE**

Range: -55 °C to +155 °C

#### **POWER RATING**

Each type rated power at 70 °C: VT1206=1/4 W

## **R**ATED VOLTAGE

The DC or AC (rms) continuous working voltage corresponding to the rated power is determined by the following formula:

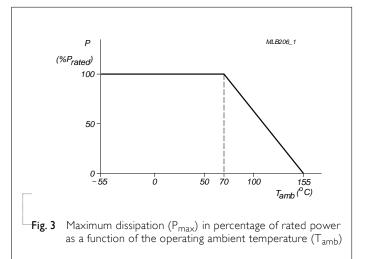
 $V = \sqrt{(P \times R)}$ 

Or max. working voltage whichever is less Where

V=Continuous rated DC or AC (rms) working voltage (v)

P=Rated power

R=Resistance value ( $\Omega$ )



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# TESTS AND REQUIREMENTS

Table 4 Test condition, procedure and requirements

EST	TEST METHOD	PROCEDURE	REQUIREMENTS
Short Time	IEC60115-14.13	2.5 times of rated voltage or maximum	±(0.05%+0.05Ω)
Overload		overload voltage, the less of the above, for 5 sec at room temperature	
High Temperature Exposure	AEC-Q200 Test 3 MIL-STD-202 Method 108	1,000 hours at Tamb = 155 °C, unpowered	±(0.3%+0.05Ω)
Moisture Resistance	AEC-Q200 Test 6 MIL-STD-202 Method 106	Each temperature / humidity cycle is defined at 8 hours (method 106F), 3 cycles / 24 hours for 10d. with 25 °C / 65 °C 95% R.H, without steps 7a & 7b, unpowered Parts mounted on test-boards, without condensation on parts	±(0.1%+0.05Ω)
Biased Humidity	AEC-Q200 Test 7 MIL-STD-202 Method 103	I,000 hours; 85 °C / 85% RH I 0% of operating power Measurement at 24±4 hours after test conclusion	±(0.1%+0.05Ω)
Life	AEC-Q200 Test 8 MIL-STD-202 Method 108	1,000 hours at 70±5 °C, RCWV applied for 1.5 hours on, 0.5 hour off, still air required	±(0.1%+0.05Ω)
Resistance to Soldering Heat	AEC-Q200 Test 15 MIL-STD-202 Method 210	Condition B, no pre-heat of samples Lead-free solder, 260±5 °C, 10±1 seconds immersion time Procedure 2 for SMD: devices fluxed and cleaned with isopropanol	±(0.05%+0.05Ω)
Thermal Shock	AEC-Q200 Test 16 MIL-STD-202 Method 107	-55/+125 °C Number of cycles is 300. Devices mounted Maximum transfer time is 20 seconds. Dwell time is 15 minutes. Air – Air	±(0.1%+0.05Ω) No visible damage
Solderability - Wetting	AEC-Q200 Test 18 J-STD-002	<ul> <li>Electrical Test not required Magnification 50X</li> <li>SMD conditions:</li> <li>(a) Method B, aging 4 hours at 155 °C dry heat, dipping at 235±3 °C for 5±0.5 seconds.</li> <li>(b) Method B, steam aging 8 hours, dipping at 215±3 °C for 5±0.5 seconds.</li> <li>(c) Method D, steam aging 8 hours, dipping at 260±3 °C for 7±0.5 seconds</li> </ul>	Well tinned (>95% covered) No visible damage

Chip Resistor Surface Mount	VT	SERIES	1206
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TEST	TEST METHOD	PROCEDURE	REQUIREMENTS
Board Flex / Bending	AEC-Q200 Test 21 AEC-Q200-005	Chips mounted on a glass epoxy resin PCB (FR4) Bending for I 206: 2mm Holding time: minimum 60 second	±(0.1%+0.05Ω)
Temperature Coefficient of Resistance (T.C.R.)	IEC 60115-1 4.8	At +25/–55 °C and +25/+125°C Formula: T.C.R= $\frac{R2 - RI}{RI(t2 - tl)} \times 10^{6}(ppm/°C)$	Refer to table 2
		Where t1=+25 °C or specified room temperature t2=-55 °C or +125 °C test temperature R1=resistance at reference temperature in ohms R2=resistance at test temperature in ohms	
Flower of Sulfur	ASTM-B-809-95* * Modified	Sulfur 750 hours, 105°C, unpowered.	±(2.0%+0.05Ω)



# <u>REVISION HISTORY</u>

REVISION	DATE	CHANGE NOTIFICATION	DESCRIPTION
Version 0	Feb. 24, 2023	-	- First issue of this specification

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