

CUSTOMER	
CUST. PART NO.	
CUST. DOC. REV.	
DESCRIPTION	POWER CHOKE(RoHS+H.F.)
SAMPLE LOT NO.	
PART NO.	CSCA2016D-XXXX-LRH
DOC. REV.	
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Customer Signature:	Date:
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☐This part currently development section.	Production line can produce this series of products.
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CUSTOMER	CUSTOMER P/N	REV.	SPL.	LOT NO.	<u> </u>	
PART NAME POWER CHOKE (RoHS+H.F.)	PART NO. CSCA2016D-XXXX-LRH			Q'TY 0 PCS		
ENO REVISION	GINEERING CHAN	GE NO	DTIC	E - RECO AUTHOR	ORD DATE	REMARK
	CONNEL CO	侵害 股份有限 SA TEM ALLIANC	Contraction Ltd. Eliter	AICOERVEU.		



5. Electrical Characteristics:

	Nominal Inductance (uH)	Inductance Tolerance	DC Resistance	Reted Current (mA) Max		Measuring	
Part Number.			(Ω)			Frequency (MHz)	
			Мах	ldc1	ldc2		
CSCA2016D-R24M-LRH	0.24	±20%	0.042	4200	3000	2	
CSCA2016D-R47M-LRH	0.47	±20%	0.046	2800	2800	2	
CSCA2016D-R68M-LRH	0.68	±20%	0.065	2350	2350	2	
CSCA2016D-1R0M-LRH	1.0	±20%	0.075	2200	2200	2	
CSCA2016D-1R5M-LRH	1.5	±20%	0.130	1600	1650	2	
CSCA2016D-2R2M-LRH	2.2	±20%	0.160	1500	1500	2	
CSCA2016D-3R3M-LRH	3.3	±20%	0.255	1150	1200	2	
CSCA2016D-4R7M-LRH	4.7	±20%	0.380	1000	950	2	

Maximum rated voltage: DC25V

*)The saturation current value (ldc1) is the maximum DC current value having inductance decrease down to 30% (at 20 deg C) *)The temperature rise current value (ldc2) is the maximum DC current value having temperature increase up to 40degC. (at 20 deg C)

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*)The rated current is following either Idc1 or Idc2, which is the lower one.

%Caution for Temperature Rise.

Temperature rise of this inductor depends on the installed board condition. It shall be confirmed in the actual end product that temperature rise of inductor is within operating temperature.

*Operating temperature: -40℃ ~105℃

Storage temperature: -40 $^{\circ}$ C ~ +85 $^{\circ}$ C

6. Structural Drawing:



- 1. Core
- Coil material
- 3. Over-coating resin
- Electrode

Metal magnetic Polyurethane-copper wire Epoxy resin, containing metal powder Base material : Ag Foundation plating : Ni Surface plating : Sn

7. Characteristic Curve

DC Current Characteristics



L VS Frequency Current Characteristics



9. Reflow Profile Chart (Reference):



	Test Item	Standard	Test method
	Resistance to flexure substrate	No damage:	The test samples shall be soldered to the testing board and by reflow soldering conditions as show in page5 (Reflow profile chart). Apply pressure in the direction of the arrow until bent width reaches 2 mm.
			Pressure Rod Board
			Test Sample 45±2 Unit : mm
(0			Substrate size:100*40*1.0
00			Substrate material :glass epoxy-resin
IST			Solder cream thickness :0.12
ЦЦ			(Land size refer to recommended land pattern
CT	Adhesion of	No abnormality:	The test samples shall be soldered to the testing heard and by reflow
CHARA	terminal electrode	No abhornaitty.	soldering conditions as shown page5 (Reflow profile chart).
ICAL (- FF	有 信 Annua
HAN		HE ALE FI	
MEQ		tym in the	Duration:5 s.
		1114 680	Solder cream thickness:0.12mm
		<u>44</u>	(Land size refer to recommended Land pattern defined of "Precaution")
	Body strength	No damage:	Applied force :10 N Duration :10 s
		E S	R0. <u>5 mm</u>
		Poppority	Dielec Sample
		-mirol	ZECTRICS C

lest item	Standard			Tes	st method		
Resistance to	Inductance change:	The test samples shall be soldered to testing jig as shown in un table.				wn in unde	
vibration	Within±10%						
	No abnormality	Freque	ency range	10~55Hz			tion
	observed in	Overal	I Amplitude	1.5mm(Sna 196 m/S ²)	all not excee	d accelera	lion
	appearance.	Sweep	ing Method	10 to 55 to	10 Hz for 1	min.	
		-	Time	2 hours ea	ch in X, Y, a	nd Z direct	ion.
Resistance to	Inductance change:	3 time of	f reflow over	n at 230 deg	C min for 40	sec max.	
addaring	within±10%	with peak temperature at 260+0/-5 deg				5 sec max	
soldering	No abnormality	Substrate thickness. 1.0mm					
	observed in appearance.	Substrat	e material .g	Jiass epoxy.	16311		
Solderability	At least 90% of terminal	The test	samples sha	all be subm	erged molter	n solder as	shown in u
,	electrode is covered by	table.		ion with 050			
		Flux: me	solder: Sn-	ion with 25% 3Aq-0.5Cu}	% of rosin or	equivalent	
	new solder.	Solder	Temperatur	e 245	5±5 dea C		
		1t	Time		5±0.5s.		
	13年	Imme	rsing Speed	1 2	5 mm/s		
	KETTE	{Eutectic solder}					
	the second	Solder Temperature 230±5 deg C Time 5±0.5s.					
	+++/1 18m)						
	<u>I</u> III	Imme	rsing Speed	2	5 mm/s		
Temperature	Inductance change: within+15%	Measure	ement shall b d the value :	be taken in a at +20 deg(temperatur	e range of	-40 degC to dard value
	No abnormality.						
Thormal aboald	Observed in appearance.	The test	aamalaa ah		ad to the to	ting iig on	d by roflow
I nermai shock	Within±10%	soldering	conditions	as shown ir	page5 (Ref	low profile	chart).
	No abnormality	The test	samples sha	all be left fo	r the specifie	d time at e	each of
	observed in appearance.	tempera	ture in steps	from 1 to 4	, as shown i	n under tak	ole in seque
		The tem	perature cyc	les shall be	repeated 10	0 cycled ir	n the Metho
		Contaitio			Time	(min)	1
		1	-40±3 (deg C	30:	<u></u> ±3	1
		2	Room	Temp	With	in 3	1
		3	85±2 c	dea C	30:	±3	
		4	Room	Temp	With	in 3	
				•			-
Low	Inductance change:	The test	samples sha	all be solde	red to the tes	sting jip an	d by reflow
temperature life	within±10%	soldering	g conditions	as shown ir	n page5 (Ref	low profile	chart).
lesi	observed in appearance.	And after that proceed the test as shown condition Temperature -40±2 deg C		ndition und T	er table.		
				4			
			rime	500 -	⊦∠4/-U N	J	

	Test Item	Standard	Test method					
	Hihg temperature life test	Inductance change: within±10% No abnormality	The test samples shall be soldered to the testing jig and by reflow soldering conditions as shown in page5 (Reflow profile chart).					
		observed in appearance.	Temperature -85±2 deg C					
			Time 500+24h					
	Damp heat life	Inductance change:	The test samples shall be soldered to the testing iig and by reflow					
Ś	test	within±10%	soldering conditions as shown in page5 (Reflow profile chart).					
L TES		No abnormality observed in appearance.	The test samples shall be put in thermostatic oven set at temperature with humidity as shown in under table.					
IEN			Temperature 60±2 deg C					
NNC			Humidity 90~95%RH					
VIRC		AB F	「「月」「「Time 500+24 h					
Ш		KUNKEN Z	周瓷股份本 学 计					
	Loading under	Inductance change: The test samples shall be soldered to the testing jig and by reflow						
	test	No abnormality observed in appearance.	The test samples shall be put in thermostatic oven set at temperature with humidity, as shown in under table and with the rated current continuously applied.					
		H S	Temperature 60±2 deg C					
		Po Cri	Humidity 90~95%RH					
		SPEDIT	Diele Current Refer to Page 3					
			Time 500+24 h					
Sta	ndard							
me	asuring huition	less otherwise specified, at midity after the test. followe	t least 2 hrs of recovery under the room temperature and normal ed by the measurement within 48 hrs					
	·							





