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# PRODUCT SPECIFICATION

PRODUCT: CERAMIC DISC CAPACITOR SAFETY RECOGNIZED

**TYPE: AS SERIES** 

**CUSTOMER:** 

DOC. NO.: POE-D18-00-E-05

## APPROVED BY CUSTOMER

PSA
PASSIVE SYSTEM ALLIANCE

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■ WALSIN TECHNOLOGY CORPORATION

566-1, KAO SHI ROAD, YANG-MEI

TAO-YUAN, TAIWAN

□ PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

NO.277,HONG MING ROAD,EASTERN SECTION, GUANG ZHOU ECONOMIC AND TECHNOLOGY DEVELOPMENT ZONE,CHINA

DONGGUAN WALSIN TECHNOLOGY ELECTRONICS CO., LTD.

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### **MANUFACTURE SITE:**

V PAN OVERSEAS (GUANGZHOU) ELECTRONIC CO.,LTD.

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## **Record of change**

Date	Version	Description	page
2014/11/19	0	First edition.	all
2016/1/27	1	<ol> <li>Review the Available lead code of Lead Configuration.</li> <li>Revised standard NO. of VDE.</li> </ol>	5 9
2019/1/18	2	Revised recognized NO. of CQC and ENEC (DEMKO).	9
2019/4/1	3	1. Add CQC approval marking on the body.	8
2019/4/24	4	<ol> <li>"Protrusion length": "2.0max (Or the end of lead wire may be inside the tape.)" revised to "+0.5to-1.0 (Or the end of lead wire may be inside the tape.)"</li> <li>Add "Soldering Recommendation"</li> </ol>	7 18
2019/12/11	5	<ol> <li>Review the Available lead code of Lead Configuration</li> <li>Add "8.3 Label samples"</li> </ol>	5 14





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BE PASSIVE SYSTEM ALL TANCE SEE	
	Part number for SAP system  Mechanical  Part numbering/T.C/Capacitance/ Tolerance/Diameter  Taping format  Marking  Scope  Specification and test method  Packing specification  Notices  Soldering Recommendation



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## 1. Part number for SAP system:

$(\mathbf{Ex.})$	YU	0AS	472	<u>M</u>	<u> 14</u>	0	$\mathbf{L}$	20	<u>C</u>	0	<u>H</u>
	<b>(1)</b>	(2)	(3)	<b>(4)</b>	<b>(5)</b>	<b>(6)</b>	<b>(7</b> )	(8)	<b>(9</b> )	<b>(10)</b>	(11)

(1)Temperature characteristic (identified code)

CODE	Temperature characteristic	Cap. Change
YP	Y5P	±10%
YU	Y5U	-55% to +20%

(2)TYPE (identified by 3-figure code) :  $0AS = AS Type(X1:760V \sim /Y1:500V \sim)$ ,

(3)Capacitance (identified by 3-figure code):EX.472=4700pF

(4)Capacitance tolerance (identified by code): K:±10%,M:±20%

(5) Nominal body diameter dimension (identified by 2-figure code): 07--Dmax8.0mm, 08--Dmax9.0mm...

(6)Internal code: 0--Normal, other code--Special control

(7)Lead Style: Refer to "2. Mechanical".

(8)Packing mode and lead length (identified by 2-figure code)

Taping Code	Description
AM	Ammo box and product pitch: 25.4 mm
	大学 七股份×

Bulk Code	Description
03	Lead length : 3.0mm
3E	Lead length : 3.5mm
04	Lead length : 7.4.0mm SYSTEM ALLIA
4E	Lead length : 4.5mm
20	Lead length : 20mm

## (9)Length tolerance

Code	Description no logy							
A	±0.5 mm (only for kink lead type)	Short lead						
В	±1.0 mm	Short lead						
С	Min.	Long lead						
D	Taping special purpose	Taping						

### (10)Pitch

Code	Description
0	10±1 mm
A	10±0.5 mm
С	12.5± 0.8 mm

## (11)Epoxy Resin Code

Code	Description
Н	Halogen and Pb free, epoxy resin.



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## 2. Mechanical:

Encapsulation: Epoxy resin, flammability UL94 V-0

## Available lead code (unit: mm):

Lead type	SAP P/N (13-17)digits	Pitch (F)	Lead Length (L)	Packing	Lead Configuration			
	L03B0	$10 \pm 1.0$	$3.0 \pm 1.0$					
	L4EB0	$10 \pm 1.0$	$4.5 \pm 1.0$		D max. T max.			
	L05B0	$10 \pm 1.0$	$5.0 \pm 1.0$					
Lead style: L or B	L03BC	$12.5 \pm 0.8$	$3.0 \pm 1.0$	Bulk	( ) For			
Type L or B Straight lead	L3EAC	$12.5 \pm 0.8$	$3.5 \pm 0.5$		L≧20mm			
	L4EBC	$12.5 \pm 0.8$	$4.5 \pm 1.0$		° + F - + + + + + + + + + + + + + + + + +			
	L20C0	$10 \pm 1.0$	20 min.		For L<20mm			
	L20CC	$12.5 \pm 0.8$	20 min.					
	BAMD0	10 ± 1.0	Refer to "4. Taping format"	Tap. Ammo				
	G03B0	$10 \pm 1.0$	$3.0 \pm 1.0$		D max.			
Lead style: G	G4EB0	10 ± 1.0	行有±1.0 亿	Bulk				
Type G Straight lead	G05B0	10 ± 1.0	$5.0 \pm 1.0$	RY CAN	e to the second			
	GAMD0	10 ± 1.0	Refer to "4. Taping format"	Tap. Ammo	Ø d→			
	D03A0	$10 \pm 1.0$	$3.0 \pm 0.5$	, O.	D may I may			
	D3EA0	$10 \pm 1.0$	$3.5 \pm 0.5$		D max.			
Lead style: D	D04A0	$10 \pm 1.0$	$4.0 \pm 0.5$	10 00				
Type D	D03AC	$12.5 \pm 0.8$	$3.0 \pm 0.5$	Bulk				
Vertical kink	D3EAC	$12.5 \pm 0.8$	3.5 ± 0.5	1101.	Omax.			
lead	D04AC	$12.5 \pm 0.8$	$4.0 \pm 0.5$		F F - 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1			
	DAMD0	10 ± 1.0	Refer to "4. Taping format"	Tap. Ammo	Ø d L ød			
	X03A0	$10 \pm 1.0$	$3.0 \pm 0.5$		D max. T max.			
	X3EA0	10 ± 1.0	$3.5 \pm 0.5$		$\overline{}$			
Lead style: X	X04A0	$10 \pm 1.0$	$4.0 \pm 0.5$	Bulk	( )			
Type X	X03AC	$12.5 \pm 0.8$	$3.0 \pm 0.5$		× + ()			
Outside kink lead	X3EAC	$12.5 \pm 0.8$	$3.5 \pm 0.5$		5.0 max.			
	X04AC	$12.5 \pm 0.8$	$4.0 \pm 0.5$		ST F T T T T T T T T T T T T T T T T T T			
	XAMD0	10 ± 1.0	Refer to "4. Taping format"	Tap. Ammo	[] a d+[]+ [ <u>[]</u> -			

<sup>\*</sup> Lead diameter Φd: 0.55 +/-0.05mm

<sup>\*</sup>e (Coating extension on leads): 3.0mmMax for straight lead style, not exceed the kink for kink lead.



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## 3. Part numbering/T.C/Capacitance/ Tolerance/Diameter:

					Dimension (unit:mm)				
SAP P/N	T.C.	Capacitance(pF)	Tolerance	D(max.)	T(max.)		F	Φd	
				D(Illax.)	I (Illax.)	Bulk	Tap.	Ψα	
YP*AS101K070*		100 pF		8.0					
YP*AS151K070*		150 pF		8.0					
YP*AS221K070*		220 pF		8.0					
YP*AS331K070*	Y5P	330 pF	±10%	8.0					
YP*AS471K080*		470 pF	1070	9.0					
YP*AS561K090*		560 pF		10.0		10.0 Or 12.5			
YP*AS681K090*		680 pF		10.0	5.5		10	0.55+/-0.05	
YP*AS102K110*		1000 pF	1	12.0	3.3		10	0.55+/-0.05	
YU*AS102M080*		1000 pF		9.0		12.3			
YU*AS152M090*		1500 pF		10.0					
YU*AS222M120*	Y5U	2200 pF	±20%	13.0					
YU*AS332M120*		3300 pF	±20%	13.0					
YU*AS392M130*		3900 pF		14.0					
YU*AS472M140*		4700 pF		15.0					

• The minimum thickness of coating (reinforced insulation) is 0.4mm.

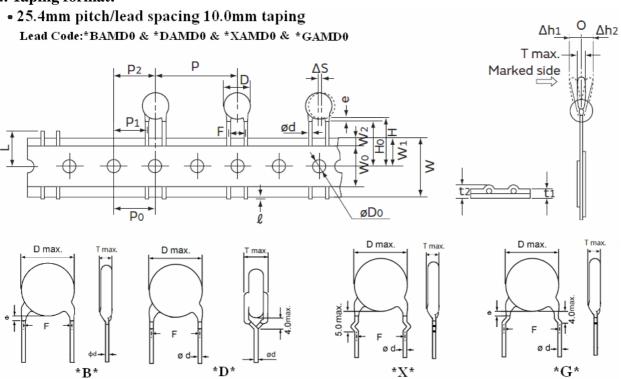




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## 4. Taping format:



POE Part Numb	er 4-x1	*BAMD0/*DAMD0/*XAMD0/*GAMD0
Item 1527 at 1547	Symbol	Dimensions(mm)
Pitch of component	//// P	25.4 ± 2
Pitch of sprocket	P0	12.7 ± 0.3
Lead spacing	Assive	SYSTEM ALLIANCE $-10.0 \pm 1.0$
Length from hole center to component center	P2	12.7 ± 1.5
Length from hole center to lead	P1	$7.7 \pm 1.5$
Body diameter	McDech	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"
Deviation along tape, left or right	$\Delta S_{\gamma_{NQ}}$	$0 \pm 2.0$
Carrier tape width	WCAMO	18.0 +1/ -0.5
Position of sprocket hole	W1	$9.0 \pm 0.5$
Lead distance between the kink and center of sprocket hole	Н0	18.0 +2.0/-0 (For: *DAMD0 & *XAMD0 & *GAMD0)
Lead distance between the bottom of body and the center of sprocket hole	Н	20.0+1.5/-1.0 (For: *BAMD0)
Length from the terminal of the lead wire to the edge of carrier tape	$\ell$	+0.5 to -1.0 (Or the end of lead wire may be inside the hole-down tape.)
Diameter of sprocket hole	D0	$4.0 \pm 0.2$
Lead diameter	φd	0.55 ±0.05
Total tape thickness	t1	$0.6 \pm 0.3$
Total thickness, tape and lead wire	t2	1.5 max.
Deviation across tape	$\triangle$ h1/ $\triangle$ h2	2.0 max.
Portion to cut in case of defect	L	11.0 max.
Hole-down tape width	W0	8.0 min
Hole-down tape distortion	W2	$1.5 \pm 1.5$
Coating extension on leads	e	3.0 max for straight lead style; Not exceed the kink leads for kink lead.
Body thickness	T	See the "3. Part numbering/T.C/Capacitance/ Tolerance/Diameter"



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5. Marking:

1.Type Designation	AS			
2.Nominal Capacitance	Identified by 3-Figure Code. Ex. 100pF→"101", 4700pF→"472"			
3.Capacitance Tolerance	K:±10%,M:±20%			
4.Company Name Code(Trade mark)	<b>K</b>			
5. Products ID	Abbreviation ex.:  Manufacture year:  9 C 6 1234			
6.Approved Monogram:	/ <b>37</b> 7- ♠ IEC 60384-14			
1) VDE approval mark Class code: X1: 760V~, Y1: 500V~				
(2) UL approval mark	<b>Fig.</b> or <b>c Fig.</b>			
FAIT				
	Marking sample			
Two sides m (for SAP part number 10-11	One side marking  (for SAP part number 10-11  digits ≤ "07" products)  digits ≥ "08" products)			
AS101K X1:760V~ Y1:500V~	9 <u>C</u> 61234			
* Marking by the laser.				
* "C": Marked with code "_" si	tand for Halogen and Pb free epoxy resin.			

\* " • ": Individual specification code, it is added under the lot no.



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## 6. Scope:

This specification applies to ceramic insulated capacitors disk type used in electronic equipment.

## 6.1Applicable safety standard

This specification applies to the VDE, ENEC10,UL/CUL approved ceramic capacitors disc type for antenna coupling, line-by-pass and across-the-line. X1, Y1 capacitor based on IEC60384-14.

## 6.2 Safety standards approval and recognized no.

Safety Standard	Standard No.	Subclass	W.V.	Recognized No.
,		X1	760VAC	E146544
UL/CUL	ANSI/UL 60384-14:2013	Y1	500VAC	(FOWX2/FOWX8)
VDE	EN 60384-14:2013	X1	760VAC	
(ENEC)	IEC60384-14:2013	Y1	500VAC	40039265
ENEC	EN 60384-14:2013/ A1:2016, EN	X1	760VAC	ENEC-01964-A1
(DEMKO)	60384-14:2013	Y1	500VAC	ENEO-01304-A1
CQC	GB/T6346.14-2015	X1	760VAC	CQC18001186964
- 540	05,10010.14 2010	当的自	500VAC	04010001100004





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### 7. Specification and test method:

7.1 Operating Temperature Range: -40 to +125 $^{\circ}$ C

#### 7.2 Test condition:

Test and measurement shall be made at the standard condition. (temperature  $15\sim35^{\circ}$ C, relative humidity  $45\sim75\%$  and atmospheric pressure  $860\sim1060$ hpa). Unless otherwise specified herein.

If doubt occurred on the value of measurement, and measurement was requested by customer capacitors shall be measured at the reference condition. (temperature  $20\pm2^{\circ}\text{C}$  or  $25\pm2^{\circ}\text{C}$ , relative humidity  $60\sim70\%$  and atmospheric pressure  $860\sim1060$ hpa.)

7.3 Performance:

No	It	ems	Performance		7	<b>Festing</b>	metho	d	
7.3.1		earance imension	The appearance and dimension shall be as given in section 3.	Visual check.					
7.3.2	Ma	rking	To be easily legible.	Visual check					
		Between terminals	No failure.	The capacitors shall not be damage when AC4000V (rms.) are applied between the lead wires for 60sec. (Charge/Discharge current $\leq$ 50mA.)					
7.3.3	Withstand voltage	Body Insulation	No failure: 大有人	body of the capacitor to the distance of about 3 to 6mm from each terminal. Then, the capacitor should be inserted into a container filled with metal balls of about 1mm diameter. Finally, AC4000V (r.m.s.)<50/60Hz> is applied for 60 s between the capacitor lead wires and metal balls. (Charge/Discharge current ≤ 50mA.)			3 to 6mm Metal Balls filled with		
7.3.4	Insulation Resistance	Between terminals	10000MΩ or more. Stem All:	The insulation resistance shall be measured with DC500±50V within 60±5sec of charging.					
7.3.5	Capa	citance	Within specified tolerance.	Y5P&Y5U: The capacitance shall be measured at 20±2°C with 1kHz±20% and 5V(rms.) or less.					
7.3.6		ipation or(D.F.)	Y5P \ Y5U : D.F. ≦2.5%	orpolities of the control of the con					
		perature acteristic	Char. Capacitance Change	The capacita step specifie			ent shall	l be mad	le at each
			Y5P Within ± 10%	Step	1	2	3	4	5
7.3.7			Y5U Within ±20%	Temp.(°C)	+20±2	-25±2	+20±2	+85±2	+20±2
				Pre-treatment:  Capacitor shall be stored at 125±2°C for 1hour.then placed at *1room condition for 24±2hours before initial measurements.					
7.3.8	Solderability	of Leads	Lead wire should be soldered with uniform coating on the axial direction over 3/4 of the circumferential direction.	The lead wire of capacitor should be dipped into molten solder for $5 \pm 0.5$ sec.  The depth of immersion is up to about 1.5 to 2.0 mm from the root of lead wires.  Temp. of solder: Lead Free Solder (Sn-3Ag-0.5Cu) $245\pm5^{\circ}$ C					

<sup>\* &</sup>quot;room condition" temperature: 15~35°C, humidity: 45~75%, atmospheric pressure: 86~106kPa

<sup>&</sup>quot;C" expresses nominal capacitance value (pF).



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No	Items		Performance	Testing method
		Tensile	Lead wire shall not cut off capacitor shall not be broken.	As shown in the figure at right, fix the body of the capacitor and apply a tensile weight gradually to each lead wire in the radial direction of the capacitor up to 10N and keep it for 10±1 sec.
7.3.9	Robustness of Terminations	Bending	Lead wire shall not cut off. Capacitor shall not be broken.	With the termination in its normal position, the specimen is held by its body in such a manner that the axis of the termination is vertical; a mass applying a force of 5N is then suspended from the end of the termination. The body of the specimen is then inclined, within a period of 2 to 3sec, through an angle of approximately 90 in the vertical plane and then returned to its initial position over the same period of time; this operation constitutes one bend. One bend immediately followed by a second bend in the opposite direction.
		Appearance	No marked defect.	As shown in figure, the lead wires should be immersed in solder of $350 \pm 10$ °C or $260 \pm 5$ °C up to 1.5 to 2.0 mm from
		I.R.	1000 MΩ min.	the root of terminal for $3.5 \pm 0.5$ sec ( $10 \pm 1$ sec. for $260 \pm 5$ °C ).
		Dielectric Strength	Per item7.3. 3	Thermal Capacitor
7.3.10	7.3.10 Soldering Effect (Non-Preheat)		Y5P,Y5U: Within ±10 %  PASSIVE SYSTEM ALL:	Pre-treatment:  Capacitor shall be stored at 125±2°C for 1hour.then placed at *1room condition for 24±2hours before initial measurements.  Post-treatment:  Capacitor shall be stored for 1 to 2hours at *1room condition.
		Appearance	No marked defect.	First the capacitor should be stored at $120+0/-5$ °C for 60 $+0/-5$ sec.  Then , as in figure , the lead wires should be immersed solder of $260+0/-5$ °C up to $1.5$ to $2.0$ mm from the root of terminal for $7.5+0/-1$ sec.  Thermal Screen 1.5
7.3.11	Soldering	I.R.	$1000~\mathrm{M}\Omega$ min.	Molten Solder
	Effect (On-Preheat)	Dielectric Strength	Per item 7.3.3	Codd
			Y5P,Y5U: Within ±10 %	Pre-treatment: Capacitor shall be stored at 125±2°C for 1hour.then placed at *1room condition for 24±2hours before initial measurements.  Post-treatment: Capacitor shall be stored for 1 to 2hours at *1room condition.

<sup>\* &</sup>quot;room condition" temperature: 15~35°C, humidity: 45~75%, atmospheric pressure: 86~106kPa



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No	Iter	ns	Performance	Testing method		
		Appearance	No marked defect.	Set the capacitor for 500±12hours at 40±2°C in 90 to 95%		
		Capacitance Change	Y5P: Within ±10% Y5U: Within ±20%	relative humidity. Pre-treatment:		
7.0.10	Humidity		Y5P,Y5U: 5.0% max.	Capacitor shall be stored at 125±2°C for 1hour.then		
7.3.12 (Under steady State)		I.R.	Y5P&Y5U: 3000MΩmin.	placed at * 1 room condition for 24±2 hours before initial measurements.		
	State)	Dielectric Strength	Per Item 7.3.3	Post-treatment:  Capacitor shall be stored for 1 to 2hours at * 1 room condition.		
		Appearance	No marked defect.	Apply the rated voltage for 500±12 hours at 40±2°C in		
		Capacitance Change	Y5P: Within ±10% Y5U: Within ±20%	90 to 95% relative humidity.  Pre-treatment:		
7.0.10	Humidity	D.F.	Y5P,Y5U: 5.0% max.	Capacitor shall be stored at 125±2°C for 1hour.then		
7.3.13	Loading	I.R.	Y5P&Y5U: 3000MΩmin.	placed at * 1room condition for 24±2hours before initial		
		Dielectric Strength	Per Item 7.3.3	Post-treatment:  Capacitor shall be stored for 1 to 2hours at *1room condition.		
		Appearance	No marked defect.	Impulse Voltage  Each individual capacitor shall be subjected to 8kV		
7.3.14	Life	Capacitance Change  I.R.  Dielectric Strength	YSP&Y5U: Within ±20%  3000MΩ min. e system ALL  Per Item 7.3 3	impulses for three times. After the capacitors are applied to life test.  VP  0.9Vp  (uF)  (uS)  (uS)  0.5Vp  1 pp  1 pp		

 $<sup>\</sup>mbox{\%}$  "room condition" temperature  $\mbox{:}\ 15\mbox{-}35\mbox{°C}$  , humidity  $\mbox{:}\ 45\mbox{-}75\mbox{\%}$  ,atmospheric pressure  $\mbox{:}\ 86\mbox{-}106\mbox{kPa}$ 

<sup>&</sup>quot;C" expresses nominal capacitance value (pF).



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No	1	Items	Performance	Testing method
7.3.15		Active nmability	The cheesecloth shall not be on fire.	The specimens shall be individually wrapped in at least one but more than two complete layers of cheesecloth. The specimens shall be subjected to 20 discharges. The interval between successive discharges shall be 5 sec. The UAC shall be maintained for 2 min after the last discharge.  Fig. 6  C1,2: 1µF±10%  C1
7.3.16	Passive	Flammability	ticcue paper chall not	The capacitor under test shall be held in the position which best promotes burning. Each specimen shall only be exposed once to flame. Time of exposure to flame: 30sec.  Length of flame: 12±1mm  Gas burner: Length 35mm min.  Inside Dia.: 0.5±0.1mm  Outside Dia.: 0.9mm max.  Gas: Butane gas Purity 95% min.  Fig. 7
7.3.17	Temperat ure Cycle	Appearance  Char. Cap. Change  Y5P \( \leq \pm 10\)%  Y5U \( \leq \pm 20\)%  I.R.  Dielectric strength	No marked defect $DF$ $DF \le 5.0\%$ $DF \le 7.5\%$ $3000MΩ$ min.	The capacitor should be subjected to 5 temperature cycles, <temperature 5cycles="" cycle="" time:="">    Step   Temperature(°C)   Time(min)     1</temperature>

<sup>%</sup> "room condition" temperature : 15~35°C, humidity : 45~75%, atmospheric pressure : 86~106kPa

<sup>&</sup>quot;C" expresses nominal capacitance value (pF).

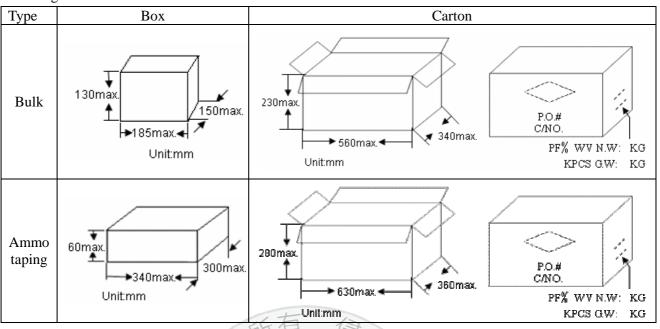


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## 8.Packing Baggage:

### 8.1 Packing size:



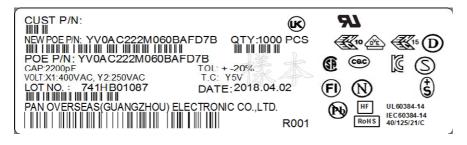
## 8.2 Packing quantity:

Packing type	The code of 14th to 15th in SAP P/N	MPQ (Kpcs/Box)
Taping	AM (The size code ≤ 11)	1
Taping	AM (The size code≥12)	0.5

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Packing type	Lead length	Size code of 10th to 11th in SAP P/N	MPQ (Kpcs/Bag)	Kpcs/Box
	Long lead	06~12	0.5	1.5
Bulk	(L≧20mm)	13-15 CO	0.5	1
	Short lead	ECHNO 06~14 DATON.	0.5	2
	(L < 20mm)	TSKPONI	0.2	1
	All	16	0.2	1

## 8.3 Label samples:





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#### 9. Notices:

## 9.1 Caution (Rating):

## (1). Operating Voltage

Be sure to maintain the Vp-p value of the applied voltage or the Vo-p which contains DC bias within the rated voltage range.

When the voltage is started to apply to the circuit or it is stopped applying, the irregular voltage may be generated for a transit period because of resonance or switching. Be sure to use a capacitor within rated voltage containing this irregular voltage.

Voltage	DC Voltage	DC+AC Voltage	AC Voltage	
Positional measurement	V0-p	V0-p	Vp-p	

## (2). Operating Temperature and Self-generated Heat

Keep the surface temperature of a capacitor below the upper limit of its rated operating temperature range. Be sure to take into account the heat generated by the capacitor itself.

## (3). Test condition for withstanding Voltage

### I. Test Equipment

Test equipment for AC withstanding voltage shall be used with the performance of the wave similar to 50/60 Hz sine waves.

If the distorted sine wave or over load exceeding the specified voltage value is applied, the defective may be caused.

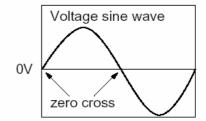
## II. Voltage Applied Method

When the withstanding voltage is applied, capacitor's lead or terminal shall be firmly connected to the output of the withstanding voltage test equipment, and then the voltage shall be raised from near zero to the test voltage.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, test voltage should be applied with the \*zero cross. At the end of the test time, the test voltage shall be reduced to near zero, and then capacitor's lead or terminal shall

be taken off the output of the withstanding voltage test equipment.

If the test voltage without the raise from near zero voltage would be applied directly to capacitor, the surge voltage may arise, and therefore, the defective may be caused.



ZERO CROSS is the point where voltage sine wave pass 0V.- See the right figure.



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#### (4). Fail-Safe

When capacitor would be broken, failure may result in a short circuit. Be sure to provide an appropriate fail-safe function like a fuse on your product if failure would follow an electric shock, fire or fume.

Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used.

## 9.2 Caution (Storage and operating condition):

Operating and storage environment

The insulating coating of capacitors does not form a perfect seal; therefore, do not use or store capacitors in a corrosive atmosphere, especially where chloride gas, sulfide gas, acid, alkali, salt or the like are present. And avoid exposure to moisture. Before cleaning, bonding, or molding this product, verify that these processes do not affect product quality by testing the performance of a cleaned, bonded or molded product in the intended equipment. Store the capacitors where the temperature and relative humidity do not exceed –10 to 40 degrees centigrade and 15 to 85 % for 6 months maximum and use within the period after receiving the capacitors.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

## 9.3 Caution (Soldering and Mounting):

## 9.3.1 Vibration and impact:

Do not expose a capacitor or its leads to excessive shock or vibration during use.

## 9.3.2 Soldering:

When soldering this product to a PCB/PWB, do not exceed the solder heat resistance specification of the capacitor. Subjecting this product to excessive heating could melt the internal junction solder and may result in thermal shocks that can crack the ceramic element.

When soldering capacitor with a soldering iron, it should be performed in following conditions.

Temperature of iron-tip: 400 degrees C. max.

Soldering iron wattage: 50W max.

Soldering time: 3.5 sec. max.

## 9.3.3 Cleaning (ultrasonic cleaning):

To perform ultrasonic cleaning, observe the following conditions.

Rinse bath capacity: Output of 20 watts per liter or less.

Rinsing time:5 min maximum.

Do not vibrate the PCB/PWB directly.

Excessive ultrasonic cleaning may lead to fatigue destruction of the lead wires.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."

### 9.4 Caution (Handling):

Vibration and impact

Do not expose a capacitor or its leads to excessive shock or vibration during use.

"Failure to follow the above cautions may result, worst case, in a short circuit and cause fuming or partial dispersion when the product is used."



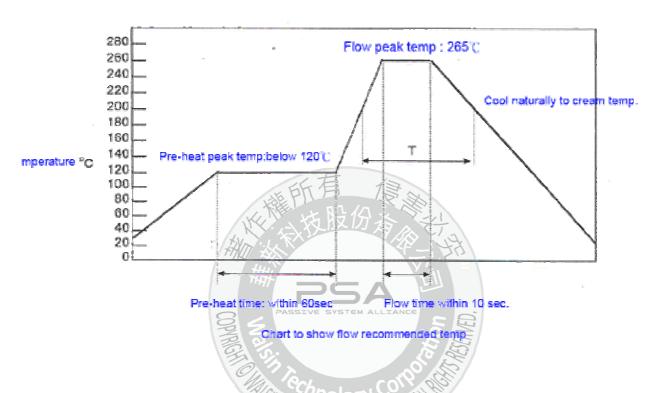
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## 10. Soldering Recommendation:

#### 10.1 Wave Soldering Profile:

- Temperature conditions of the flow is recommended as shown in the chart
- Must implement the pre-heat
- Maximum peak flow temperature is recommended 265°C
- Time "T" implement in the chart recommended within 20 sec. it temperature exceed 200°C
- Take care with the flow solder not to touch the capacitor body directly at mounting



## 10.2 Recommended Reworking Conditions with Soldering Iron:

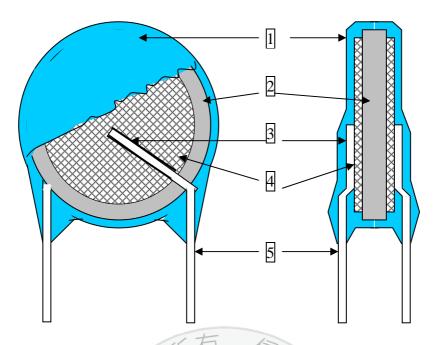
- Temperature of iron-tip: 400 degrees C. max.
- Soldering iron wattage: 50W max.
- Soldering time: 3.5 sec. max.
- Distance from coating body: 2 mm (min.)

10.3 Reflow-Soldering: Lead Ceramic Cap. should not be soldered by reflow-soldering.



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## 11. Drawing of internal structure and material list:



## Remarks:

No.	Part name	Material	Model/Type	Component
1	Insulation Coating	Epoxy polymer	EF-150 PCE-300 ECP-357	Epoxy resin, Pigment (Blue / UL 94 V-0) The minimum thickness of coating (reinforced insulation) is 0.4mm
2	Dielectric Element	Ceramic	Y5P/Y5U	BaTiO <sub>3</sub>
3	Solder	Tin-silver	Sn96.5-Ag3-Cu0.5	Sn96.5-Ag3-Cu0.5
4	Electrodes	Ag	SP-160PL SP-260PL	Silver · Glass frit
5	Leads wire	Tinned copper clad steel wire	OLOG 0.55±0.05mm	Substrate metal: Fe & Cu Surface plating: Sn 100%(3~7μm)