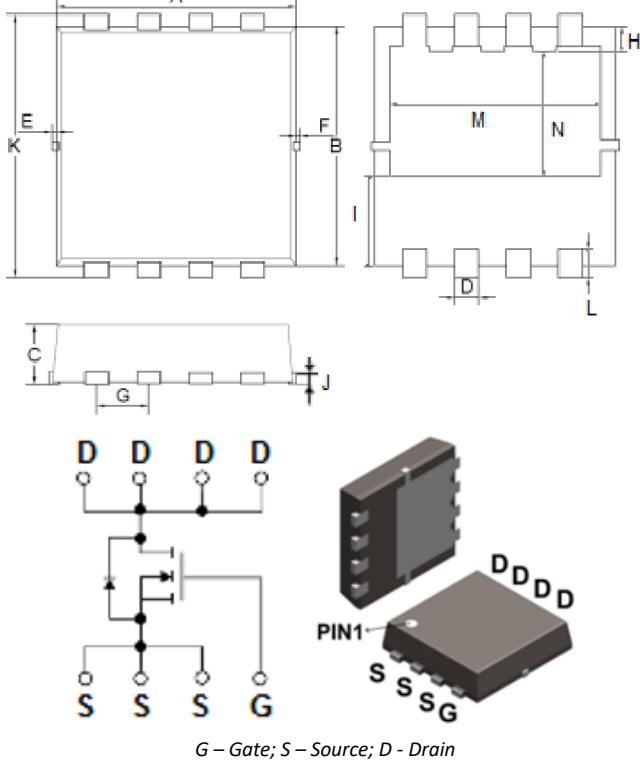


N-Channel Enhancement Mode MOSFET

Primary characteristics			
Symbol	Parameter	Value	Unit
I_D	Continuous drain current	64	A
V_{DSS}	Drain source voltage	80	V
$R_{DS(on)}$	Static drain-source on-resistance	15	$\text{m}\Omega$ MAX

Case dimensions															
															
PDFN3x3-8L															
Unit		A	B	C	D	E	F	G	H	I	J	K	L	M	N
mm		MIN 3.1	2.9	2.9	0.65 0.4	0.2 0.1	0 0.1	0.55 0.75	0.2 0.4	0.7 1.1	0.1 0.2	3.15 3.45	0.2 0.4	2.35 2.55	1.5

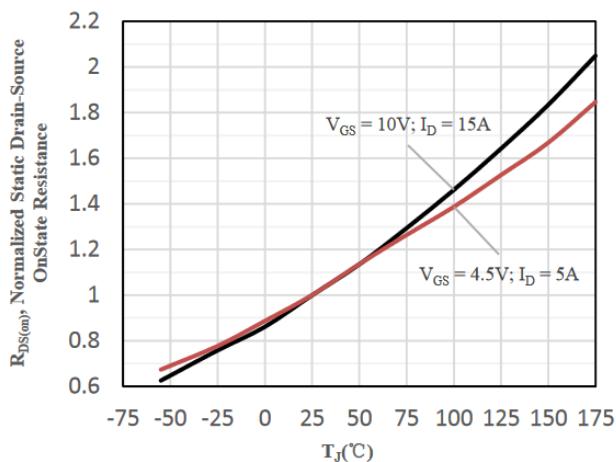
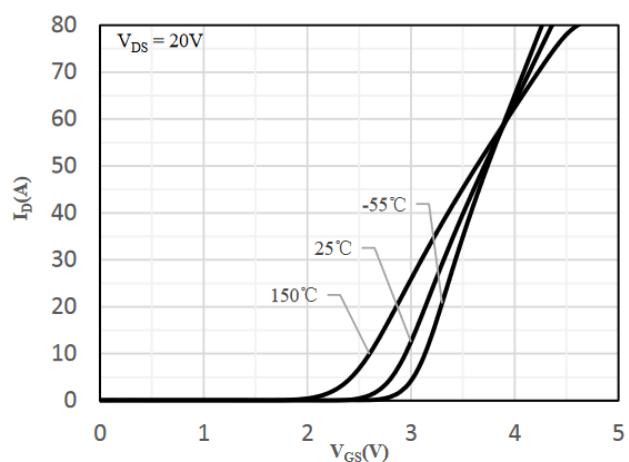
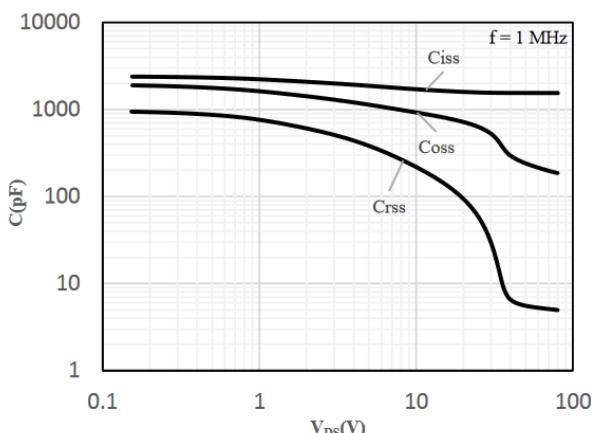
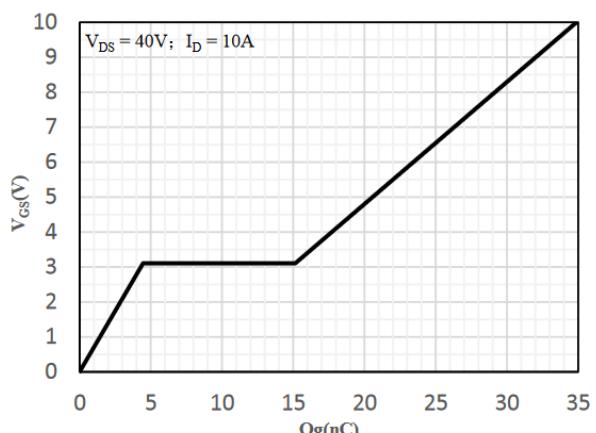
Maximum ratings ($T_A = 25^\circ\text{C}$ unless otherwise noted)								
Characteristic			Symbol	Value		Unit		
Drain-source voltage			V_{DSS}	80		V		
Gate-source voltage			V_{GSS}	± 20		V		
Continuous drain current ($T_C=25^\circ\text{C}$)			I_D	64		A		
Continuous drain current ($T_C=100^\circ\text{C}$)				41				
Continuous drain current ($T_A=25^\circ\text{C}$)				12				
Continuous drain current ($T_A=100^\circ\text{C}$)				8				
Pulsed drain current ($t_p=10\mu\text{s}$)			I_{DM}	256		A		
Single pulse avalanche energy (MAX, $V_{DD}=40\text{V}$, $V_{GS}=10\text{V}$, $L=0.5\text{mH}$)			E_{AS}	75		mJ		
Power Dissipation			P_D	57		W		
Operating junction temperature range			T_J, T_{STG}	-55 ~ 150		°C		

Thermal characteristics						
Characteristic	Test condition	Symbol	Min.	Value Typ.	Max.	Unit
Thermal resistance junction-case	-	R _{θJC}	-	2.0	2.2	°C/W
Thermal resistance junction-ambient ¹⁾		R _{θJA}	-	45	55	°C/W
Electrical characteristics (T _A = 25°C)						
Characteristic	Test condition	Symbol	Min.	Value Typ.	Max.	Unit
Drain-source breakdown voltage	V _{GS} =0V, I _D =250μA	V _{DSS}	80	-	-	V
Zero gate voltage drain current	V _{DS} =80V, V _{GS} =0V	I _{DSS}	-	-	1.0	μA
Gate body leakage current	V _{GS} =±20V, V _{DS} =0V	I _{GSS}	-	-	±100	nA
Gate threshold voltage	V _{DS} =V _{GS} , I _D =250μA	V _{GS(TH)}	1.0	1.9	2.5	V
Drain-source on-state resistance ²⁾	V _{GS} =10V, I _D =15A	R _{D(S)}	-	6.8	7.5	mΩ
	V _{GS} =6.0V, I _D =10A		-	8.0	10	
	V _{GS} =4.5V, I _D =5.0A		-	10	15	
Gate resistance	V _{GS} =0V, f=1.0MHz	R _G	-	1.8	-	Ω
Dynamic electrical characteristics						
Characteristic	Test condition	Symbol	Min.	Value Typ.	Max.	Unit
Input capacitance	V _{DS} =40V V _{GS} =0V f=1.0MHz	C _{ISS}	-	1551	-	pF
Output capacitance		C _{OSS}	-	294	-	
Reverse transfer capacitance		C _{rss}	-	7.0	-	
Switching characteristics						
Characteristic	Test condition	Symbol	Min.	Value Typ.	Max.	Unit
Turn ON delay time	V _{DS} =40V V _{GS} =15V I _D =20A R _G =3.3Ω	t _{d(ON)}	-	7.5	-	ns
Turn ON rise time		t _r	-	39	-	
Turn OFF delay time		t _{d(OFF)}	-	38	-	
Turn OFF fall time		t _f	-	50	-	
Total gate-charge	V _{DD} =40V V _{GS} =10V I _D =10A	Q _G	-	35	-	nC
Gate to source charge		Q _{GS}	-	4.4	-	
Gate to drain (Miller) charge		Q _{GD}	-	10.6	-	
Source-drain diode characteristics						
Characteristic	Test condition	Symbol	Min.	Value Typ.	Max.	Unit
Diode forward voltage ²⁾	I _{SD} =10A, V _{GS} =0V	V _{SD}	-	0.8	1.2	V
Reverse recovery time	I _{SD} =10A, V _{GS} =0V d _i /d _t =100A/μs	trr	-	40	-	Ns
Reverse recovery charge		Qrr	-	42	-	nC

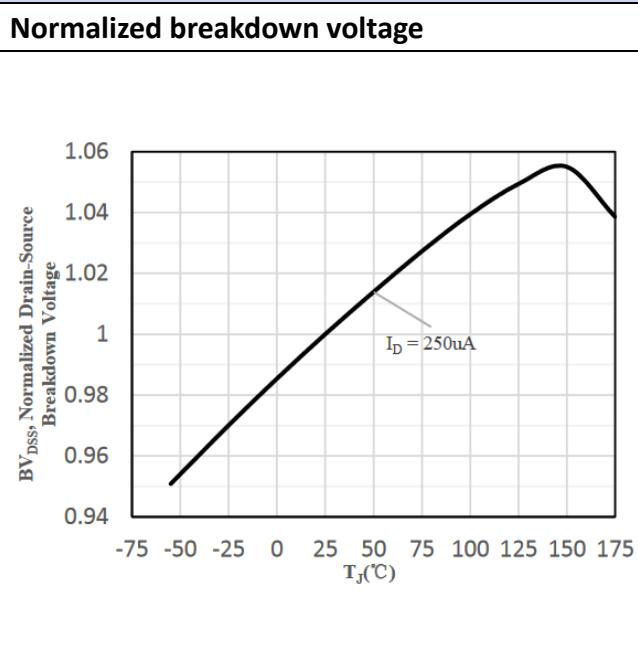
Notes:

- 1) The data tested by surface mounted on a 1.0 inch² FR-4 board with 2oz copper
- 2) The data tested by pulsed, pulse width ≤300μs, duty cycle ≤2%

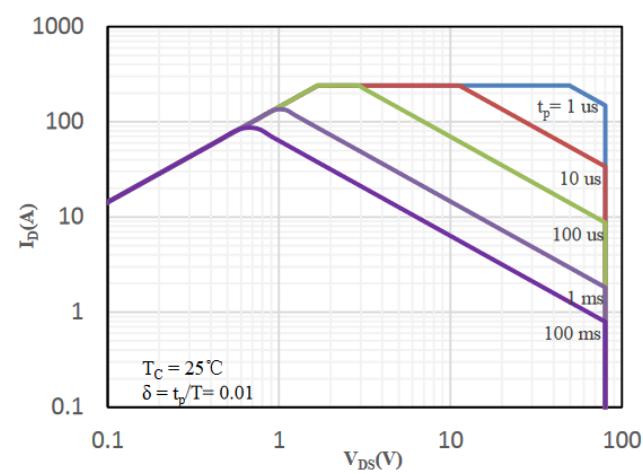
Typical characteristics	
Power dissipation	Drain current
Typical output characteristics	ON-resistance vs. drain current and gate voltage
ON-resistance vs. gate-source voltage	Body-diode characteristics

Typical characteristics
Normalized ON-resistance vs. junction temperature

Transfer characteristics

Capacitance characteristics

Gate-charge characteristics


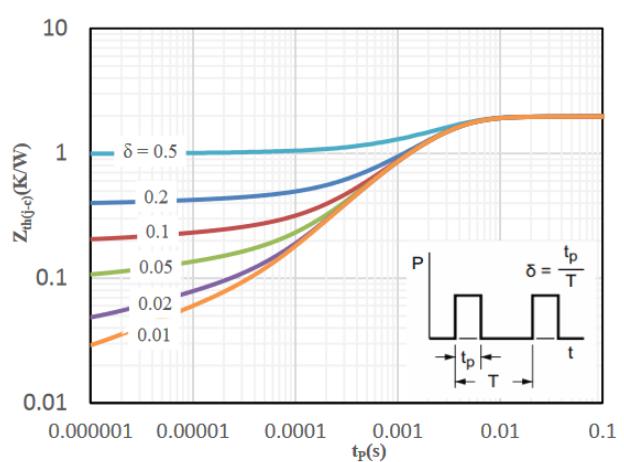
Typical characteristics

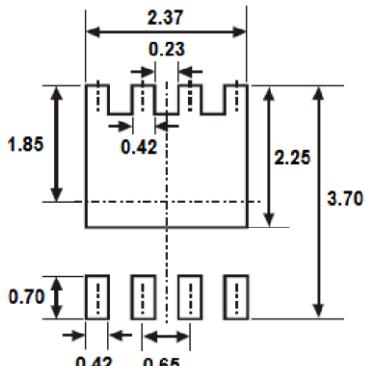


Safe operation area



Maximum transient thermal impedance



Suggested soldering pad layout

PDFN3x3-8L
Ordering information

Part Number	Marking	Package	Shipping Quantity	Dimensions
AKS075N08T-3D8	075N08T	PDFN3x3-8L	5000 pcs / reel	---

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